

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Eugene P. Marsh Group Art Unit: 2823
 Serial No.: 09/812,157 Examiner: J. Maldonado
 Filed: 19 March 2001 Docket No.: 15080930102
 Confirmation No.: 2941
 Title: METHODS FOR PATTERNING METAL LAYERS FOR USE WITH FORMING
 SEMICONDUCTOR DEVICES



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Assistant Commissioner for Patents
 Washington, D.C. 20231

We are transmitting the following documents along with this Transmittal Sheet (which is submitted in triplicate):

- ☒ An itemized return postcard.
☐ A check in the amount of \$ __, for __.
☐ A certified copy of a __ application, Serial No. __, filed __, the right of priority of which is claimed under 35 U.S.C. §119.
☒ Other: Amendment and Response including Appendix A (18 pgs).
☐ Amendment ☐ No Additional fee is required. ☐ The fee has been calculated as shown:

Fee Calculation for Claims Pending After Amendment					
	Pending Claims after Amendment (1)	Claims Paid for Earlier (2)	Number of Additional Claims (1-2)	Cost per Additional Claim	Additional Fees Required
Total Claims				x \$18 =	
Independent Claims				x \$84 =	
One or More New Multiple Dependent Claims Presented? If Yes, Add \$280 Here →					
Total Additional Claim Fees Required					

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PATENT TRADEMARK OFFICE

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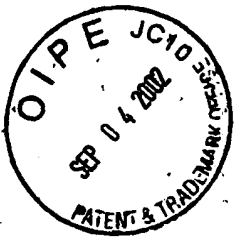
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By: SARA E. OLSON
 Name: SARA E. OLSON

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Docket No. 156-00930102

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Eugene P. Marsh) Group Art Unit: 2823
Serial No.: 09/812,157) Examiner: J. Maldonado
Confirmation No.: 2941)
Filed: March 19, 2001)
For: METHODS FOR PATTERNING METAL LAYERS FOR USE WITH FORMING SEMICONDUCTOR DEVICES

AMENDMENT AND RESPONSE

Assistant Commissioner for Patents
Washington D.C. 20231

Dear Sir:

In response to the Office Action of June 4, 2002, please amend the above-identified application as follows:

In the Specification

Please amend the Abstract beginning at page 37, line 6, as shown below. Pursuant to 37 C.F.R. §1.121, this paragraph is also shown in Appendix A with notations to indicate the changes made.

B1
-- The present invention provides a method for forming a discontinuous conductive layer in the fabrication of integrated circuits. The method includes providing a substrate assembly having a surface including at least one metal-containing adhesion region separated by at least one surface region of the substrate assembly. A conductive metal layer is formed on the surface of the substrate assembly. The substrate assembly including the conductive metal layer thereon is then annealed. Any nonadhered conductive metal is removed from the at least one exposed surface region to form a discontinuous conductive metal layer on at least one metal-containing adhesion region, for example, by simply rinsing the substrate assembly in water. The conductive metal layer can be platinum or ruthenium. --